[**Wiring Connections**](https://docs.platformio.org/en/latest/plus/debug-tools/esp-prog.html#id3)

A screen shot of a computer

Description automatically generated

| **ESP-Prog JTAG 10-Pin Connector** | **JTAG Signal** | **ESP32 Pin** | **Description** |
| --- | --- | --- | --- |
| 6 | TDO | GPIO15 | Test Data Out |
| 2 | TMS | GPIO14 | Test Mode State |
| 8 | TDI | GPIO12 | Test Data In |
| 3 | GND | GND | Digital ground |
| 4 | TCK | GPIO13 | JTAG Return Test Clock |
| 1 | VDD | VDD | Positive Supply Voltage — Power supply for JTAG interface drivers |